



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Jishnu Bhattacharjee et al.
Assignee: Scintera Networks, Inc.
Title: COMPACT, HIGH Q INDUCTOR FOR INTEGRATED CIRCUIT
Application No.: 10/718,501 Filing Date: November 19, 2003
Examiner: Not yet assigned Group Art Unit: 2811
Docket No.: M-15260 US Confirmation No.: 8235

Irvine, California
April 19, 2004

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

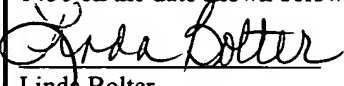
SUBMISSION OF REPLACEMENT DRAWINGS

Dear Sir:

In response to the Notice to File Missing Parts of Nonprovisional Application mailed February 19, 2004, Applicants submit five (5) sheets of replacement drawings, consisting of Figures 1-4, in compliance with 37 CFR 1.84 and 37 CFR 1.121 in the above-named application.

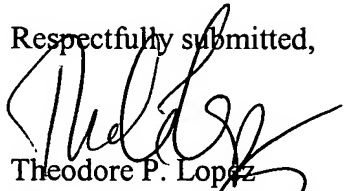
If there are any questions regarding these drawings, please call the undersigned at (949) 752-7040.

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.


Linda Bolter

April 19, 2004
Date

Respectfully submitted,


Theodore P. Lopez
Attorney for Applicants
Reg. No. 44,881

LAW OFFICES OF
McPHERSON KWOK CHEN
& HEID LLP

2402 MICHELSON DRIVE
SUITE 210
IRVINE, CA 92612
(949) 752-7040
FAX (949) 752-7049

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